

ES1 Pin	Function	Level	CPU	ADDR/-OFFSET	Name
1	GND				
2	GND				
3	SYS_5V	5V			
4	SYS_3V3	3V3			
5	ES_RX	0..5V	T17	0x870/070	UART4_RXD
6	ES_TX	0..5V	U17	0x874/074	UART4_TXD
7	LED_USER	0..5V	U18	0x878/078	GPIO1_28
8	GND				
9	ES_00	0..5V	A16	0x95c/15c	GPIO0_5
10	ES_01	0..5V	B16	0x958/158	GPIO0_4
11	ES_02	0..5V	A14	0x9ac/1ac	GPIO3_21
12	ES_03	0..5V	C12	0x99c/19c	GPIO3_17
13	GND				
14	GND				
15	ES_04	0..5V	R7	0x890/090	TIMER4
16	ES_05	0..5V	T7	0x894/094	TIMER7
17	ES_06	0..5V	T6	0x89c/09c	TIMER5
18	ES_07	0..5V	U6	0x898/098	TIMER6
19	GND				
20	GND				
21	ES_INT	0..5V	V6	0x87c/07c	GPIO1_29
22	ES_CLK	0..5V	D14	0x9b4/1b4	GPIO0_20
23	ES_RES	0..5V	T11	0x828/028	GPIO0_26
24	ES_OPT	0..5V	C18	0x964/164	GPIO0_7
25	GND				
26	GND				
27	AINP	0..5V	A8		AIN6
28	VDD_ADC	1V8			
29	AGND				
30	GND				
31	5V_inp	5V			#
32	GND				
33	5V_inp	5V			#
34	GND				

Overvoltage and Reverse Protection

M1 Pin	Function	*compatible	CPU	ADDR/-OFFSET	Name
1	AN		C8		AIN4
2	RESn	x	R12	0x834/034	GPIO1_13
3	CSn	x	T12	0x830/030	GPIO1_12
4	SCK	x	A13	0x990/190	SPI1_SCLK
5	MISO	x	B13	0x994/194	SPI1_D0
6	MOSI	x	D12	0x998/198	SPI1_D1
7	SYS_3V3	x			
8	GND	x			
9	GND	x			
10	SYS_5V	x			
11	SDA	x	D18	0x978/178	I2C2_SDA
12	SCL	x	D17	0x97c/17c	I2C2_SCL
13	TX	x	D15	0x984/184	UART1_TXD
14	RX	x	D16	0x980/180	UART1_RXD
15	INTn	x	U12	0x82c/02c	GPIO0_27
16	PWM		T10	0x824/024	EHRPWM2B

M2 Pin	Function	*compatible	CPU	ADDR/-OFFSET	Name
1	AN		B8		AIN5
2	RESn	x	U13	0x83c/03c	GPIO1_15
3	CSn	x	V13	0x838/038	GPIO1_14
4	SCK	x	A13	0x990/190	SPI1_SCLK
5	MISO	x	B13	0x994/194	SPI1_D0
6	MOSI	x	D12	0x998/198	SPI1_D1
7	SYS_3V3	x			
8	GND	x			
9	GND	x			
10	SYS_5V	x			
11	SDA	x	D18	0x978/178	I2C2_SDA
12	SCL	x	D17	0x97c/17c	I2C2_SCL
13	TX	x	B17	0x954/154	UART2_TXD
14	RX	x	A17	0x950/150	UART2_RXD
15	INTn	x	V12	0x88c/08c	GPIO2_1
16	PWM	x	U10	0x820/020	EHRPWM2A

* connected to same ports like BeagleBone clickSHIELD® (HW Rev 1.01)