

Head_Pin	\$PINS	ADDR/OFFSET	CPU	Name	ESICAPE	LIM	PUR	4D LCD-Cape	click SHIELD	Notes
P8_01				DGND	GND			GND	GND	System Ground
P8_02				DGND	GND			GND	GND	System Ground
P8_03	6	0x818/018	R9	GPIO1_6	EMMC					Used on Board
P8_04	7	0x81c/01c	T9	GPIO1_7	EMMC					Used on Board
P8_05	2	0x808/008	R8	GPIO1_2	EMMC					Used on Board
P8_06	3	0x80c/00c	T8	GPIO1_3	EMMC					Used on Board
P8_07	36	0x890/090	R7	TIMER4	ES_04	3V3				
P8_08	37	0x894/094	T7	TIMER7	ES_05	3V3				
P8_09	39	0x89c/09c	T6	TIMER5	ES_06	3V3				
P8_10	38	0x898/098	U6	TIMER6	ES_07	3V3				
P8_11	13	0x834/034	R12	GPIO1_13	M1_RST	3V3	10k		M1_RST	
P8_12	12	0x830/030	T12	GPIO1_12	M1_CS	3V3	10k		M1_CS	
P8_13	9	0x824/024	T10	EHRPWM2B	M1_PWM	3V3				
P8_14	10	0x828/028	T11	GPIO0_26	ES_RES	3V3				
P8_15	15	0x83c/03c	U13	GPIO1_15	M2_RST	3V3	10k		M2_RST	
P8_16	14	0x838/038	V13	GPIO1_14	M2_CS	3V3	10k		M2_CS	
P8_17	11	0x82c/02c	U12	GPIO0_27	M1_INT	3V3	10k		M1_INT	
P8_18	35	0x88c/08c	V12	GPIO2_1	M2_INT	3V3	10k		M2_INT	
P8_19	8	0x820/020	U10	EHRPWM2A	M2_PWM	3V3			M2_PWM	
P8_20	33	0x884/084	V9	GPIO1_31	EMMC					Used on Board
P8_21	32	0x880/080	U9	GPIO1_30	EMMC					Used on Board
P8_22	5	0x814/014	V8	GPIO1_5	EMMC					Used on Board
P8_23	4	0x810/010	U8	GPIO1_4	EMMC					Used on Board
P8_24	1	0x804/004	V7	GPIO1_1	EMMC					Used on Board

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P8_25	0	0x800/000	U7	GPIO1_0	EMMC					Used on Board
P8_26	31	0x87c/07c	V6	GPIO1_29	ES_INT	3V3				
P8_27	56	0x8e0/0e0	U5	GPIO2_22	LCD			LCD_VSYNC		Used by LCD-Cape
P8_28	58	0x8e8/0e8	V5	GPIO2_24	LCD			LCD_PCLK		Used by LCD-Cape
P8_29	57	0x8e4/0e4	R5	GPIO2_23	LCD			LCD_HSYNC		Used by LCD-Cape
P8_30	59	0x8ec/0ec	R6	GPIO2_25	LCD			LCD_EN		Used by LCD-Cape
P8_31	54	0x8d8/0d8	V4	UART5_CTSN	LCD			LCD_DATA14		Used by LCD-Cape
P8_32	55	0x8dc/0dc	T5	UART5_RTSN	LCD			LCD_DATA15		Used by LCD-Cape
P8_33	53	0x8d4/0d4	V3	UART4_RTSN	LCD			LCD_DATA13		Used by LCD-Cape
P8_34	51	0x8cc/0cc	U4	UART3_RTSN	LCD			LCD_DATA11		Used by LCD-Cape
P8_35	52	0x8d0/0d0	V2	UART4_CTSN	LCD			LCD_DATA12		Used by LCD-Cape
P8_36	50	0x8c8/0c8	U3	UART3_CTSN	LCD			LCD_DATA10		Used by LCD-Cape
P8_37	48	0x8c0/0c0	U1	UART5_TXD	LCD			LCD_DATA08		Used by LCD-Cape
P8_38	49	0x8c4/0c4	U2	UART5_RXD	LCD			LCD_DATA09		Used by LCD-Cape
P8_39	46	0x8b8/0b8	T3	GPIO2_12	LCD			LCD_DATA06		Used by LCD-Cape
P8_40	47	0x8bc/0bc	T4	GPIO2_13	LCD			LCD_DATA07		Used by LCD-Cape
P8_41	44	0x8b0/0b0	T1	GPIO2_10	LCD			LCD_DATA04		Used by LCD-Cape
P8_42	45	0x8b4/0b4	T2	GPIO2_11	LCD			LCD_DATA05		Used by LCD-Cape
P8_43	42	0x8a8/0a8	R3	GPIO2_8	LCD			LCD_DATA02		Used by LCD-Cape
P8_44	43	0x8ac/0ac	R4	GPIO2_9	LCD			LCD_DATA03		Used by LCD-Cape
P8_45	40	0x8a0/0a0	R1	GPIO2_6	LCD			LCD_DATA00		Used by LCD-Cape
P8_46	41	0x8a4/0a4	R2	GPIO2_7	LCD			LCD_DATA01		Used by LCD-Cape