

Head_Pin	\$PINS	ADDR/OFFSET	CPU	Name	ESICAPE	LIM	PUR	4D LCD-Cape	click SHIELD	Notes
P9_01					GND			GND	GND	System Ground
P9_02					GND			GND	GND	
P9_03					VCC_3V3			VDD_3V3EXP	VCC_3V3	
P9_04					VCC_3V3			VDD_3V3EXP	VCC_3V3	
P9_05					VCC_5V Inp	5V2				Polyfuse 2A and
P9_06					VCC_5V Inp	5V2				Reverse Protection
P9_07					SYS_5V			SYS_5V	VCC_5V	
P9_08					SYS_5V			SYS_5V	VCC_5V	
P9_09					PWR_BTN		10k	PWR_BUT		
P9_10			A10		SYS_RES		10k	SYS_RESETh		
P9_11	28	0x870/070	T17	UART4_RXD	ES_RX	3V3				
P9_12	30	0x878/078	U18	GPIO1_28	LED_USER	3V3		LED_USER		
P9_13	29	0x874/074	U17	UART4_TXD	ES_TX	3V3				
P9_14	18	0x848/048	U14	EHRPWM1A	LCD	3V3		PWM_Backlight	M1_PWM	Used by LCD-Cape
P9_15	16	0x840/040	R13	GPIO1_16	BTN / (DEBUG)	3V3		BTN_LEFT		Used by LCD-Cape
P9_16	19	0x84c/04c	T14	EHRPWM1B	LCD	3V3		LCD_DISP		Used by LCD-Cape
P9_17	87	0x95c/15c	A16	GPIO0_5	ES_00	3V3				
P9_18	86	0x958/158	B16	GPIO0_4	ES_01	3V3				
P9_19	95	0x97c/17c	D17	GPIO0_13	SCL	3V3	2k2	I2C2_SCL	I2C2_SCL	
P9_20	94	0x978/178	D18	GPIO0_12	SDA	3V3	2k2	I2C2_SDA	I2C2_SDA	
P9_21	85	0x954/154	B17	GPIO0_3	M2_TX	3V3			M2_TX	
P9_22	84	0x950/150	A17	GPIO0_2	M2_RX	3V3			M2_RX	
P9_23	17	0x844/044	V14	GPIO1_17	BTN / (DEBUG)	3V3		BTN_RIGHT		Used by LCD-Cape
P9_24	97	0x984/184	D15	GPIO0_15	BTN / M1_TX	3V3		BTN_ENTER	M1_TX	Disconnected by Jumper
P9_25	107	0x9ac/1ac	A14	GPIO3_21*	ES_02	3V3				

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P9_26	96	0x980/180	D16	GPIO0_14	M1_RX	3V3			M1_RX	
P9_27	105	0x9a4/1a4	C13	GPIO3_19	BTN / (DEBUG)	3V3		BTN_UP		Used by LCD-Cape
P9_28	103	0x99c/19c	C12	GPIO3_17	ES_03	3V3				
P9_29	101	0x994/194	B13	GPIO3_15	MX_MISO	3V3			M1M2_MISO	
P9_30	102	0x998/198	D12	GPIO3_16	BTN / MX_MOSI	3V3		BTN_DOWN	M1M2_MOSI	Disconnected by Jumper
P9_31	100	0x990/190	A13	GPIO3_14	MX_SCK	3V3			M1M2_SCK	
P9_32					VDD_ADC					
P9_33			C8		M1_AN	1V8				
P9_34					AGND					
P9_35			A8		AINP	1V8				
P9_36			B8		M2_AN	1V8				
P9_37			B7		LCD	1V8		TOUCH		Used by LCD-Cape
P9_38			A7		LCD	1V8		TOUCH		Used by LCD-Cape
P9_39			B6		LCD	1V8		TOUCH	M1_AN	Used by LCD-Cape
P9_40			C7		LCD	1V8		TOUCH	M2_AN	Used by LCD-Cape
P9_41A	109	0x9b4/1b4	D14	GPIO0_20	ES_CLK	3V3				2 ports connected to headerpin 41
P9_41B		0x9a8/1a8	D13	GPIO3_20						
P9_42A	89	0x964/164	C18	GPIO0_7	ES_OPT	3V3				2 ports connected to headerpin 42
P9_42B		0x9a0/1a0	B12	GPIO3_18						
P9_43					GND			GND	GND	System Ground
P9_44					GND			GND	GND	System Ground
P9_45					GND			GND	GND	System Ground
P9_46					GND			GND	GND	System Ground